

Fig. 2

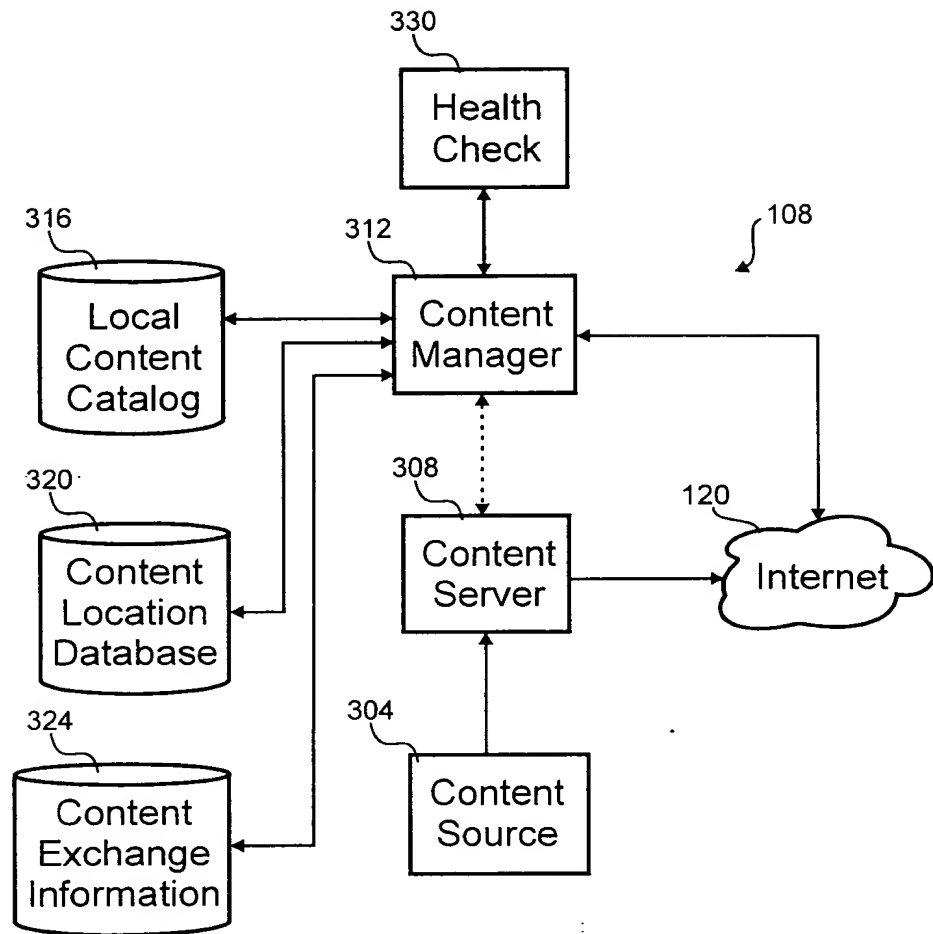


Fig. 3A

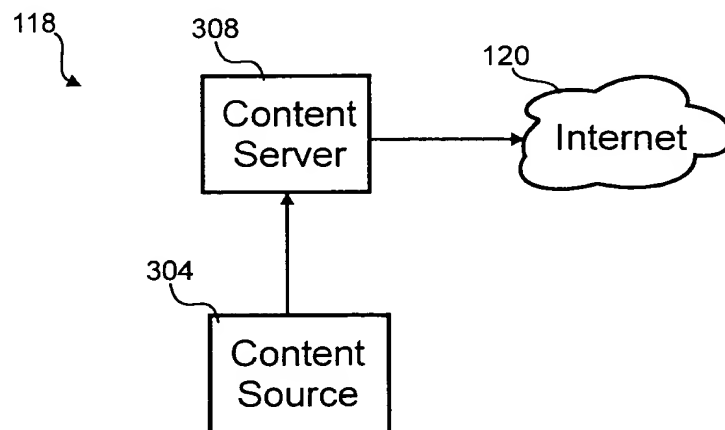


Fig. 3B

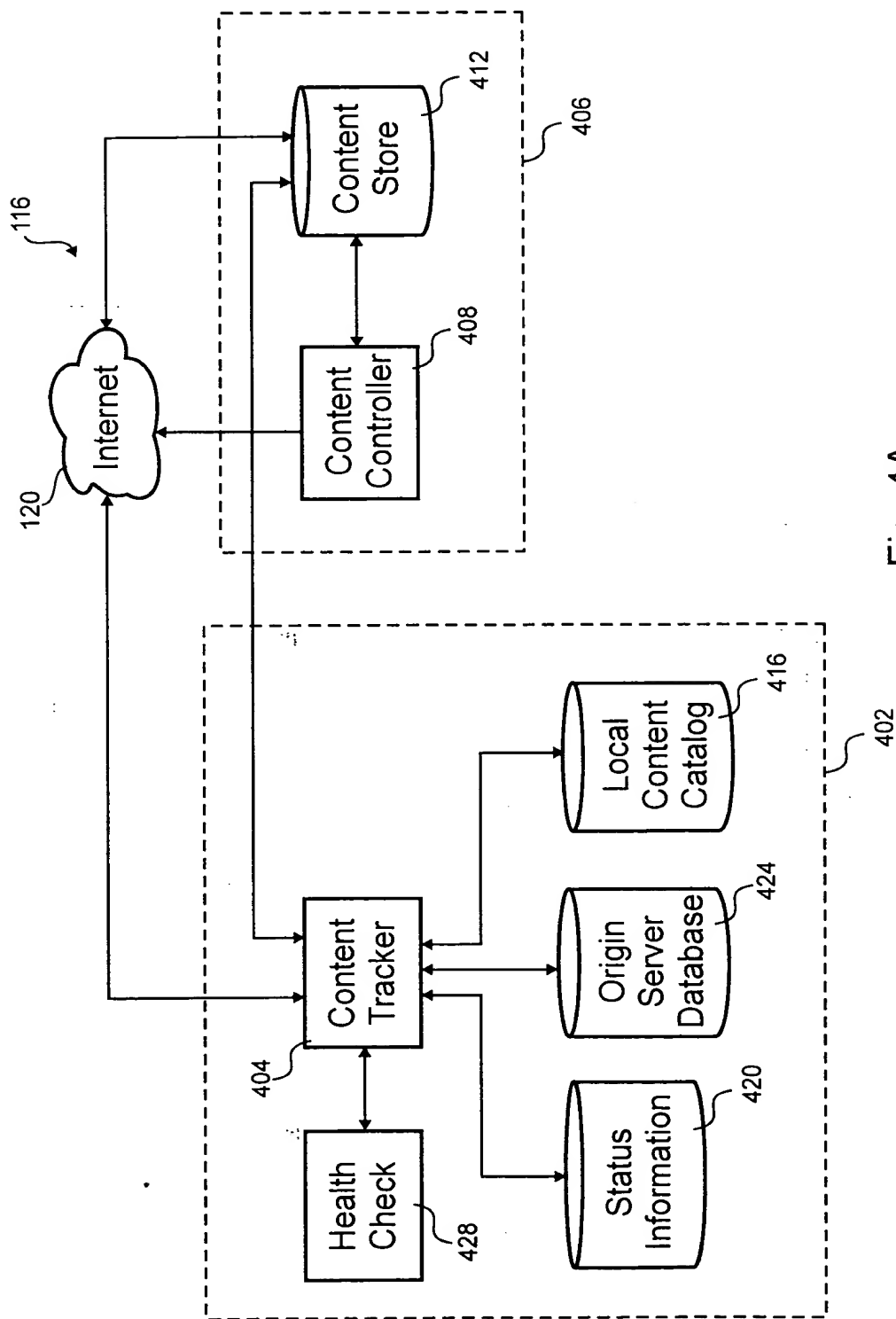


Fig. 4A

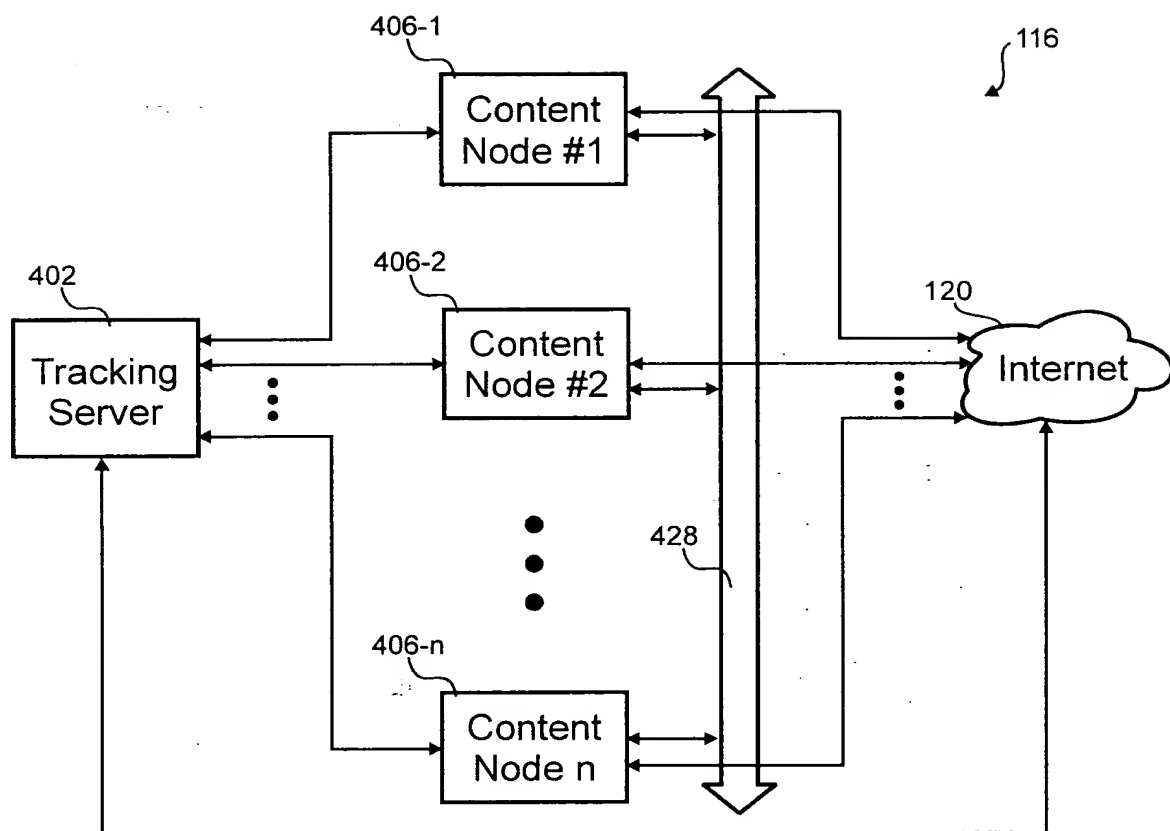
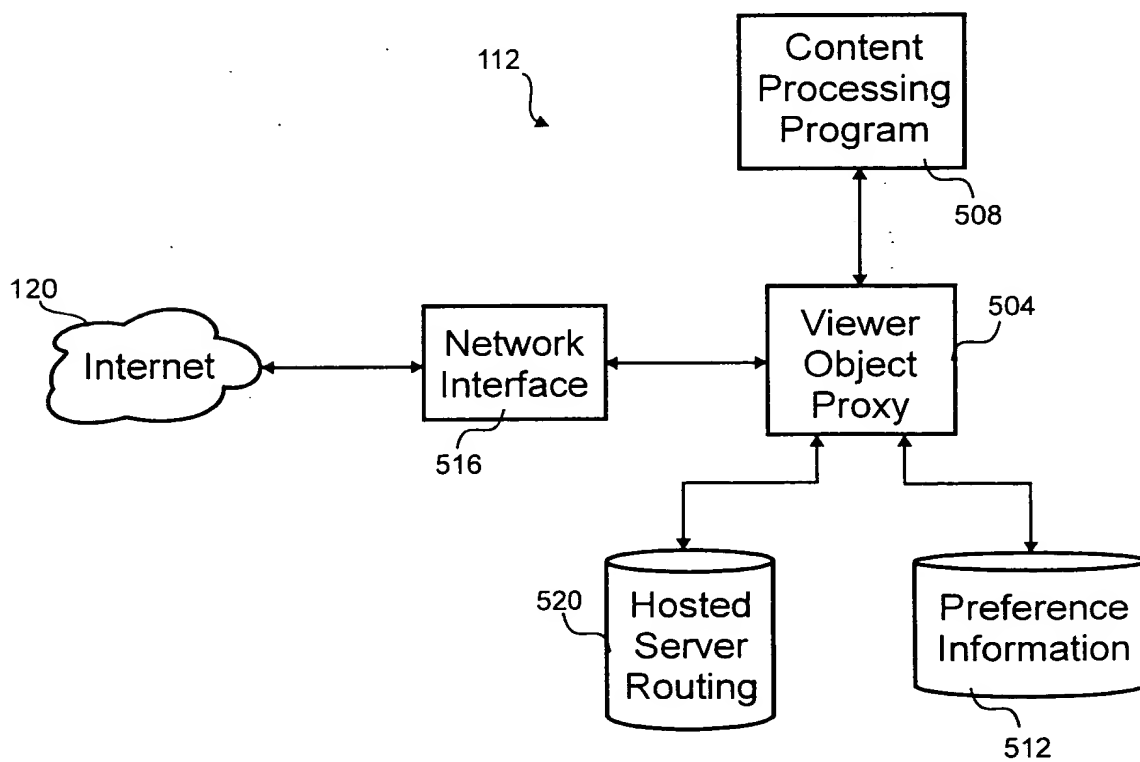
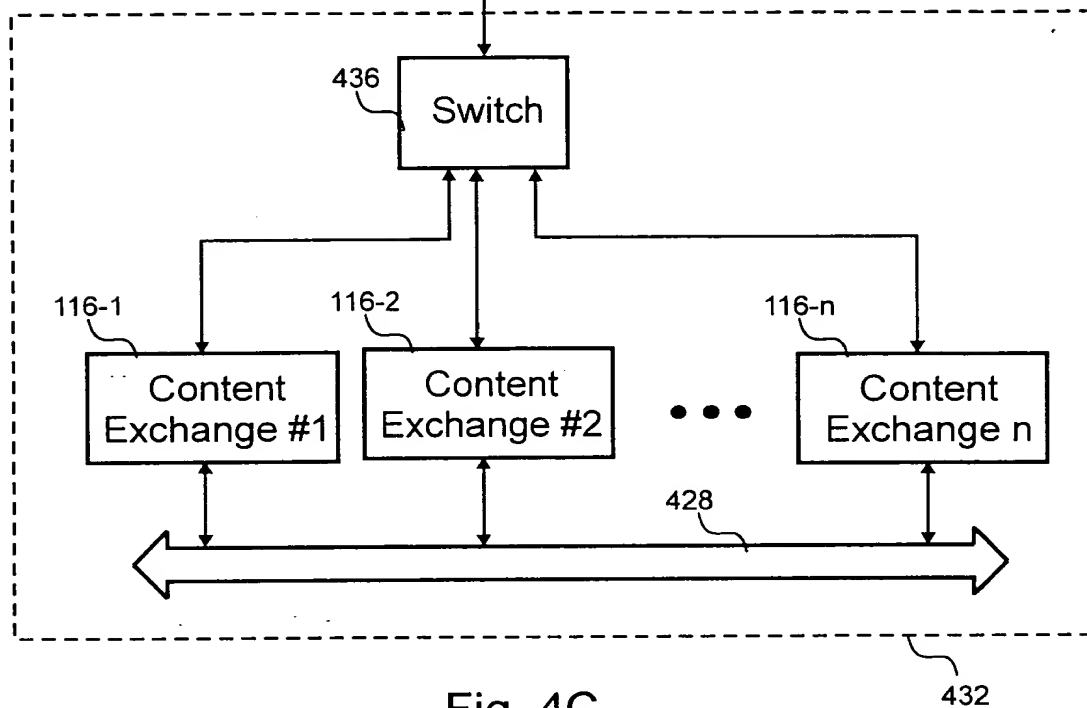


Fig. 4B



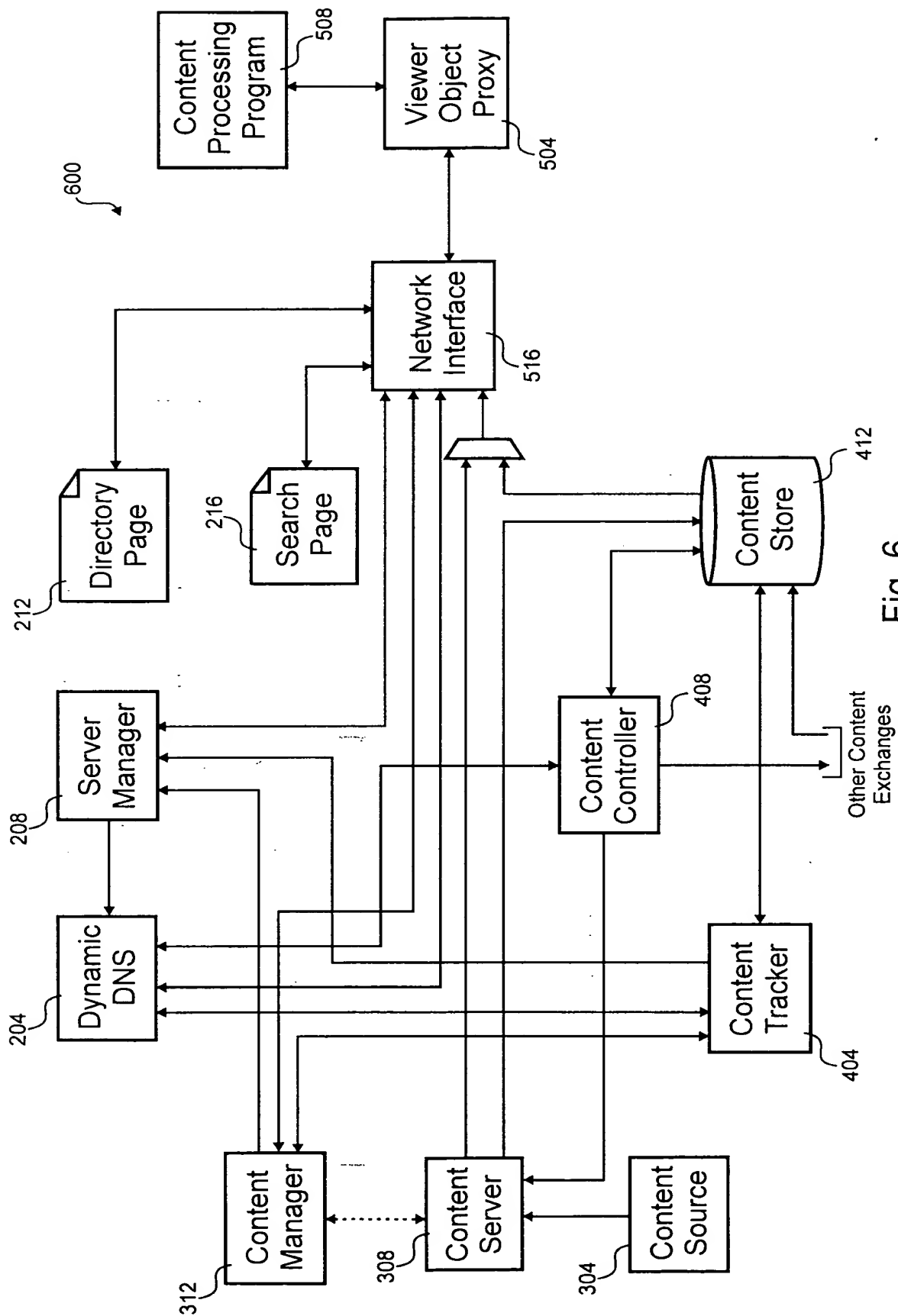


Fig. 6

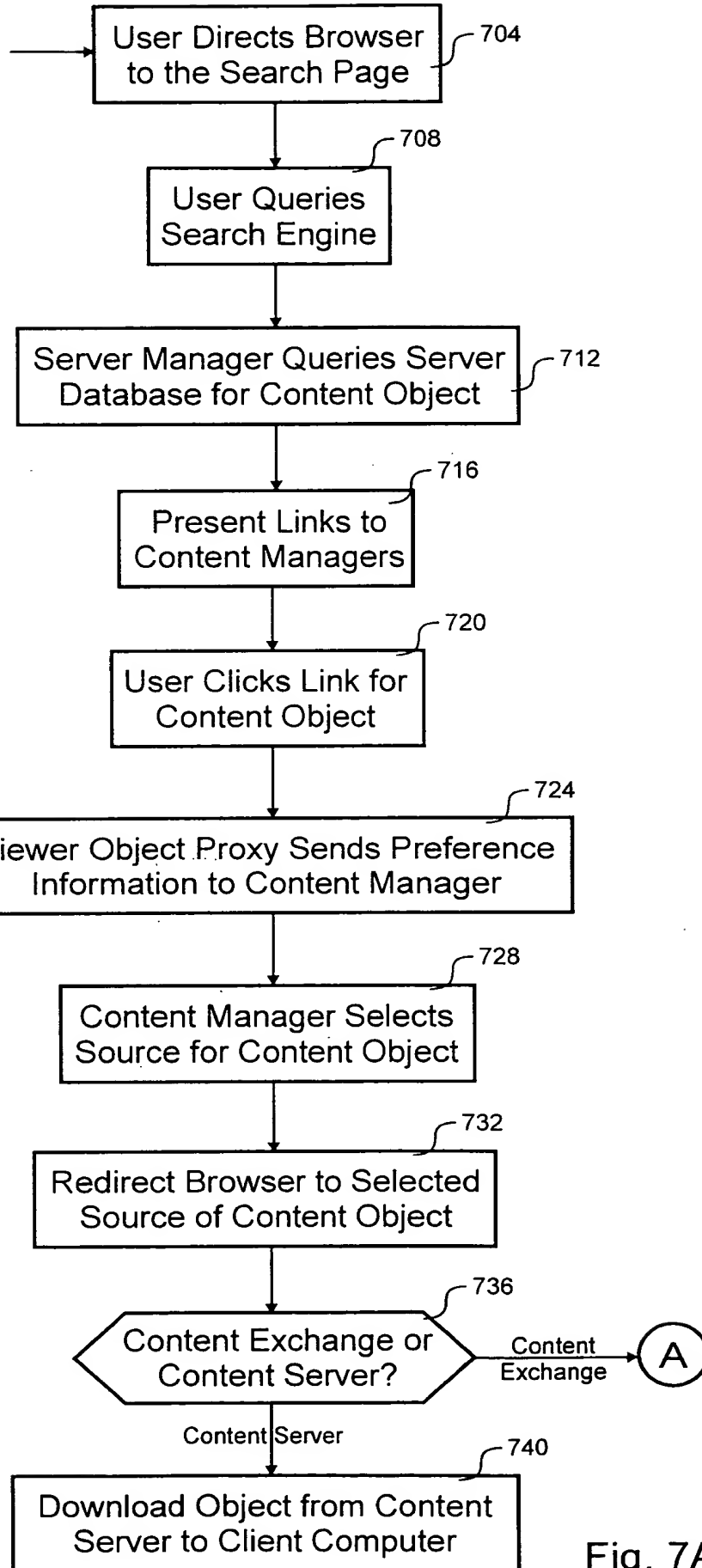


Fig. 7A



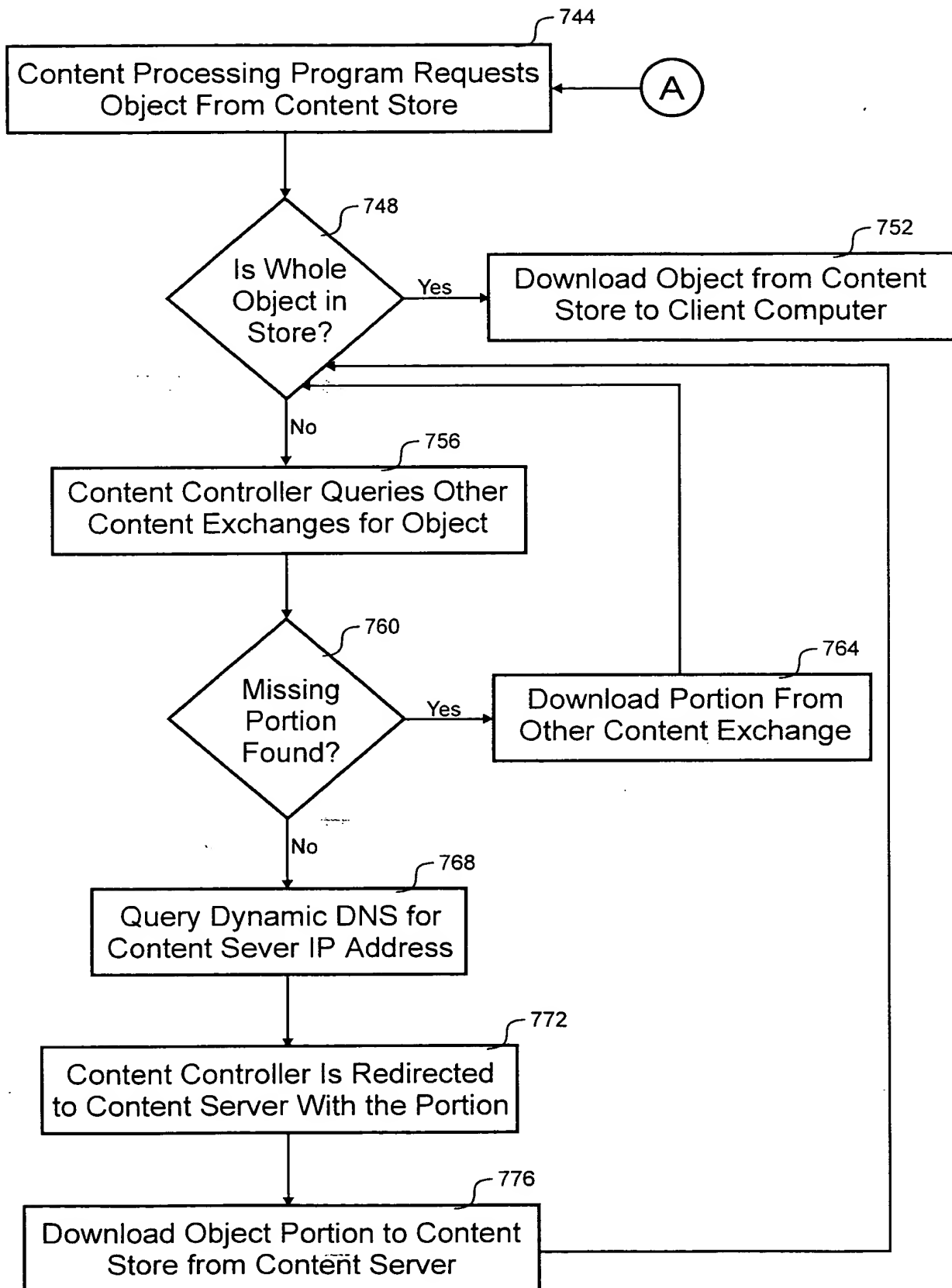


Fig. 7B

```
graph TD; MF[Main Function 868] --- SWAF[Standard Web Access Function 858]; MF --- EC[Electronic Commerce Function 866]; MF --- D[Display 854]; MF --- LALF[Look Ahead Logic Function 856]; MF --- PE[Path Evaluator 850]; MF --- SE[Security Evaluator 864]; MF --- DF[Decryption Function 862]; MF --- DecF[Decompression Function 860]; MF --- WF[Weighing Function 852]
```

The diagram illustrates a system architecture centered around a **Main Function** block (868). This central block is connected to eight peripheral functions:

- Standard Web Access Function** (858)
- Electronic Commerce Function** (866)
- Display** (854)
- Look Ahead Logic Function** (856)
- Path Evaluator** (850)
- Security Evaluator** (864)
- Decryption Function** (862)
- Decompression Function** (860)
- Weighing Function** (852)

504

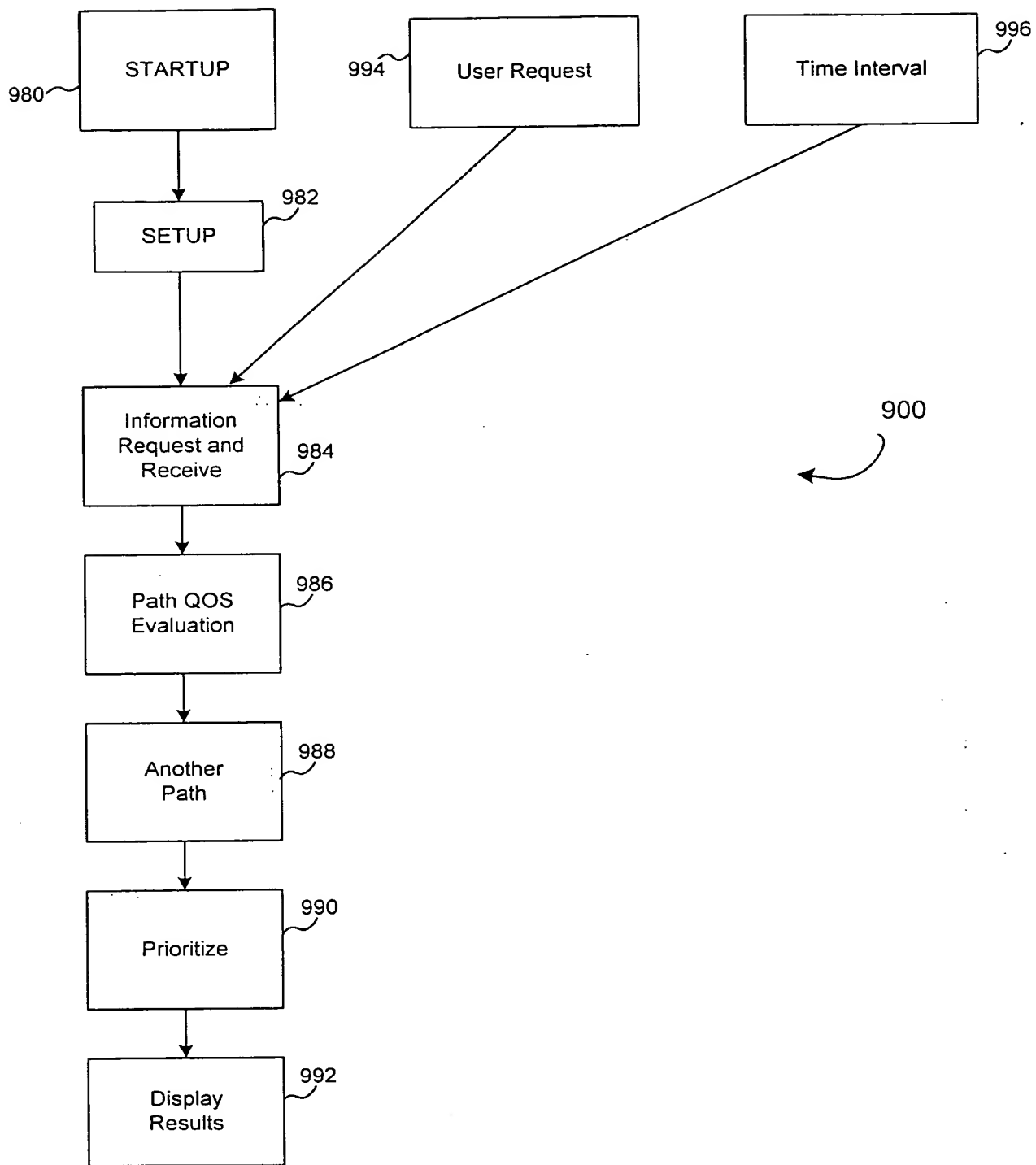


FIG. 9

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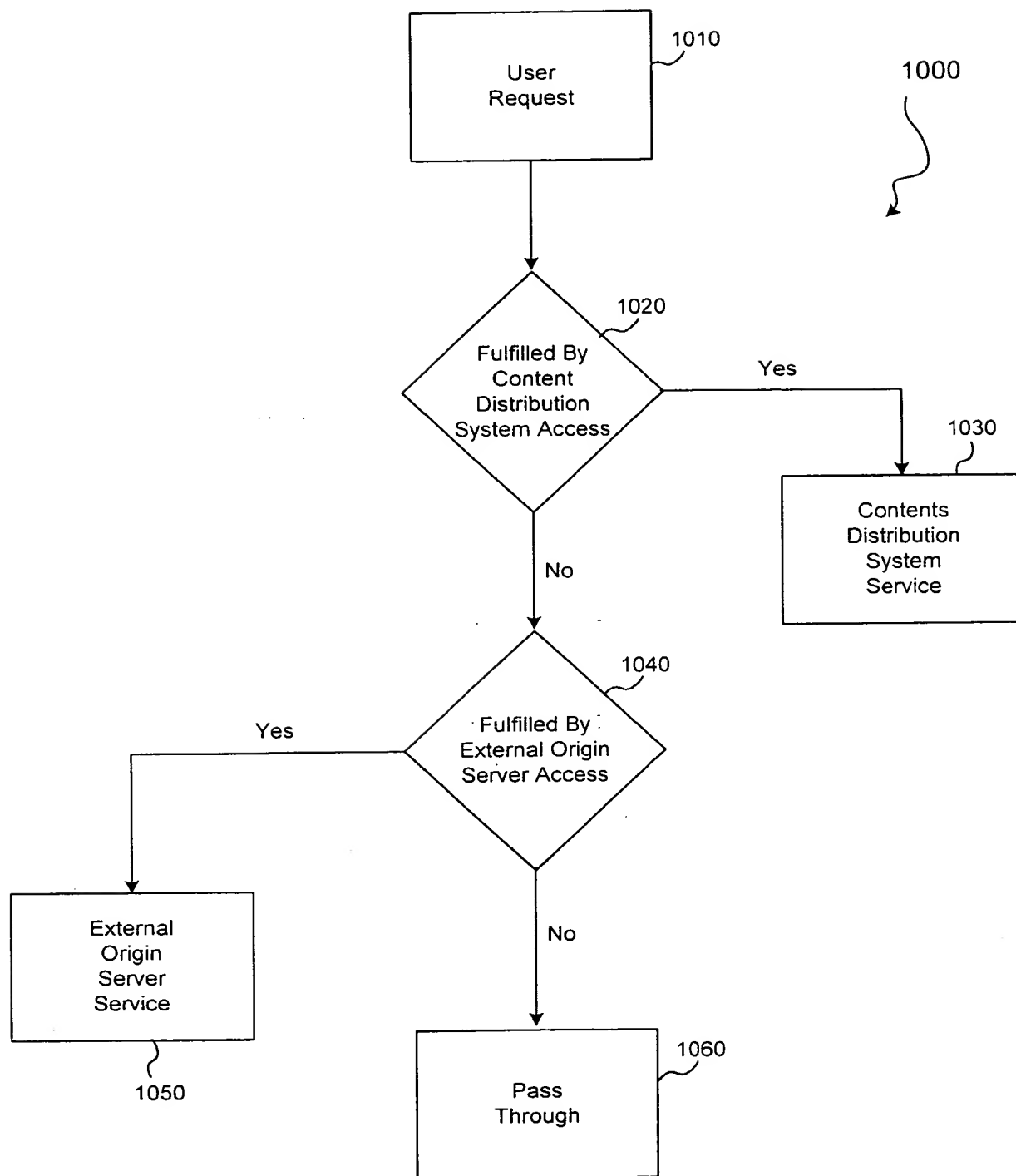


FIG. 10

Fig. 11

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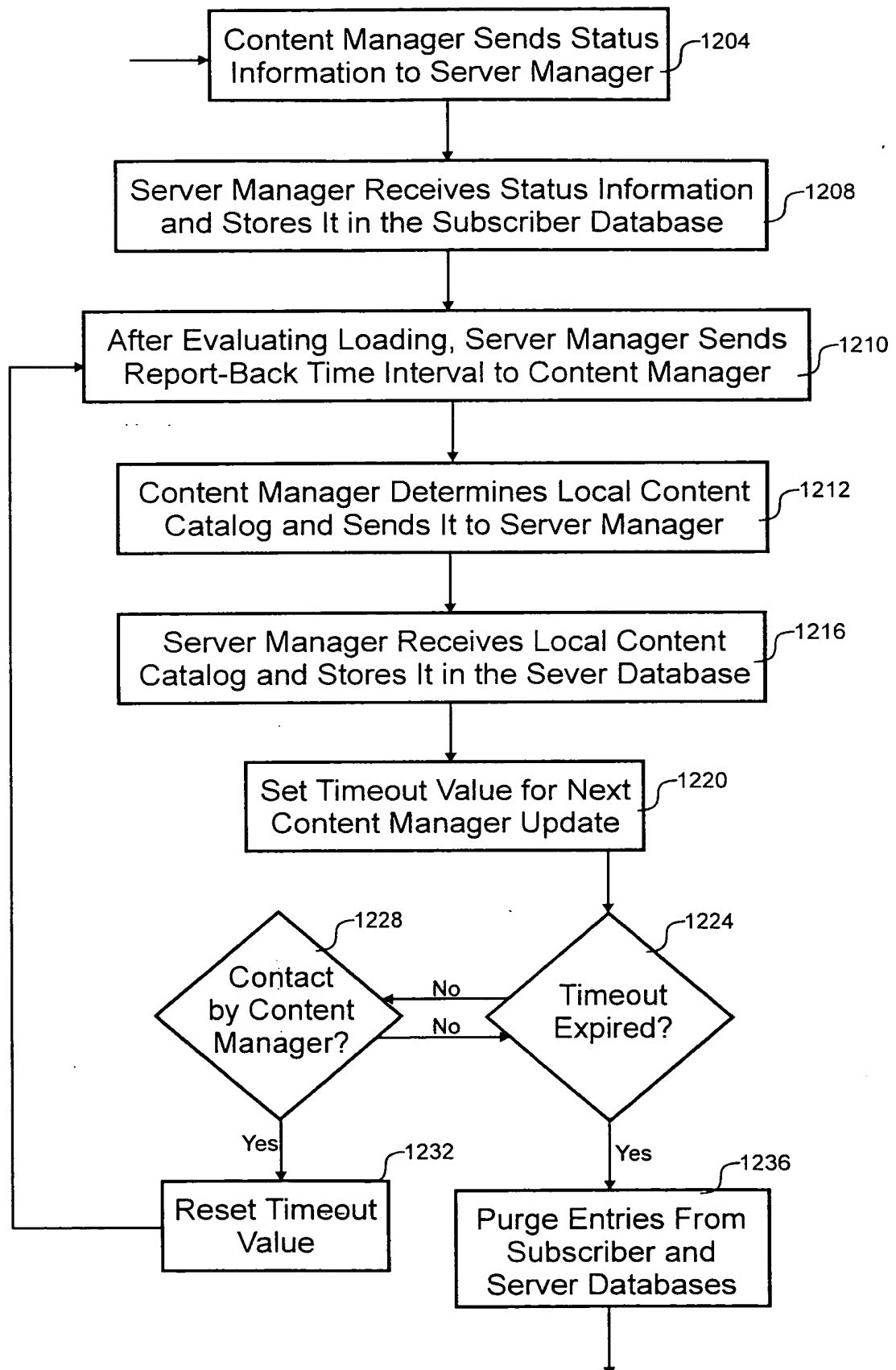


Fig. 12

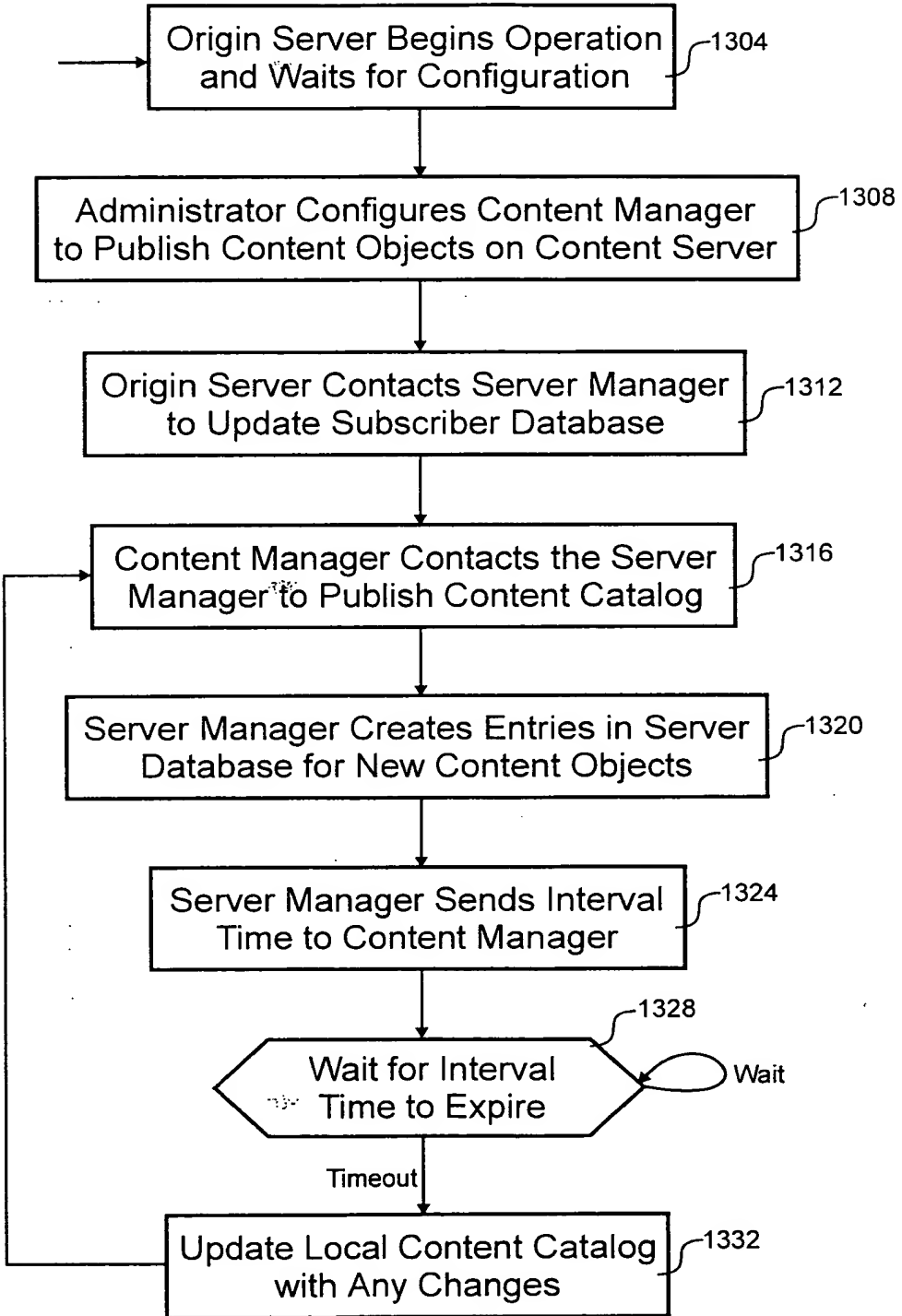
[illegible]

Fig. 13

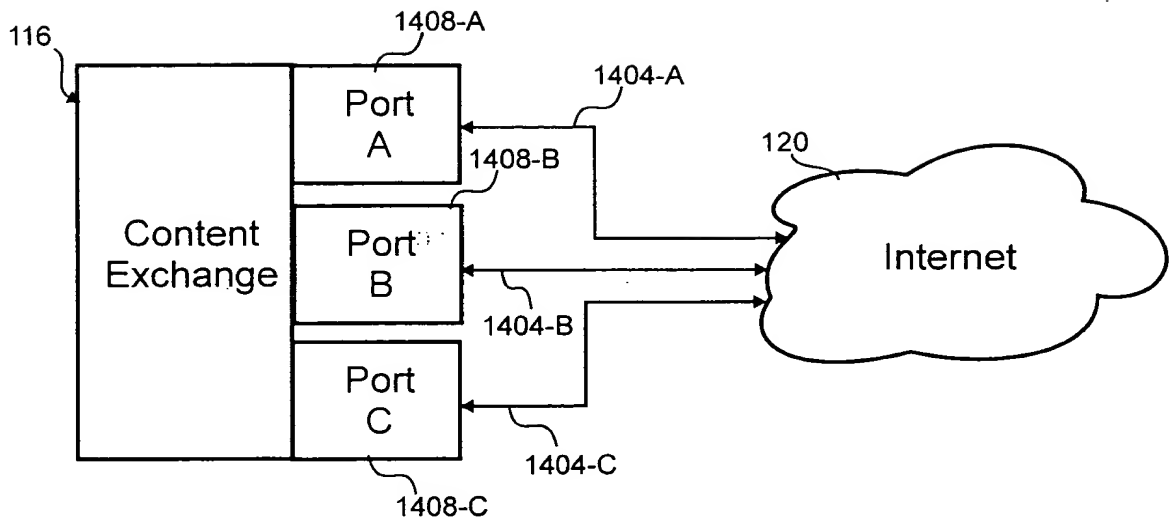


Fig. 14

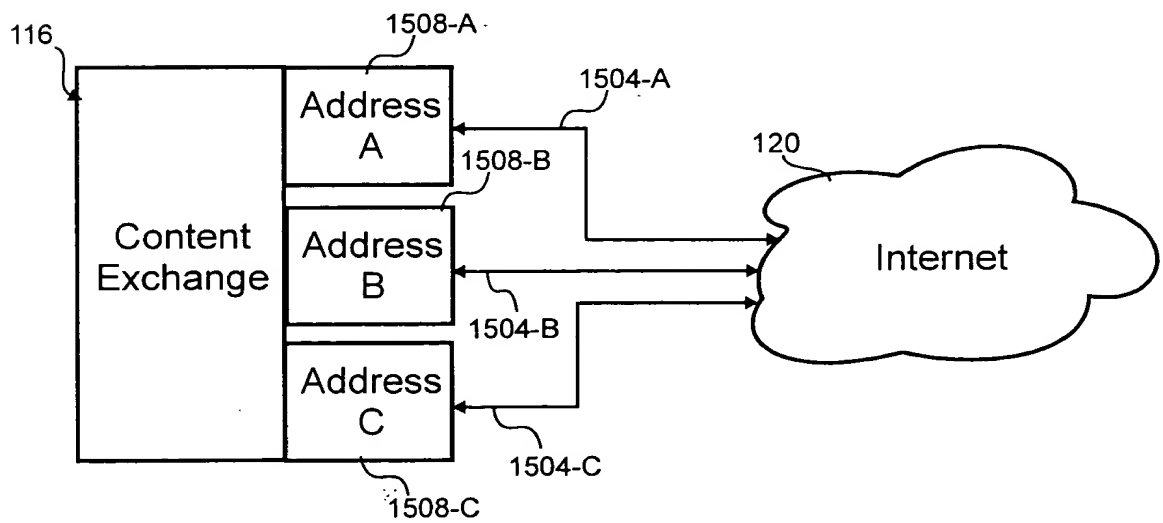


Fig. 15





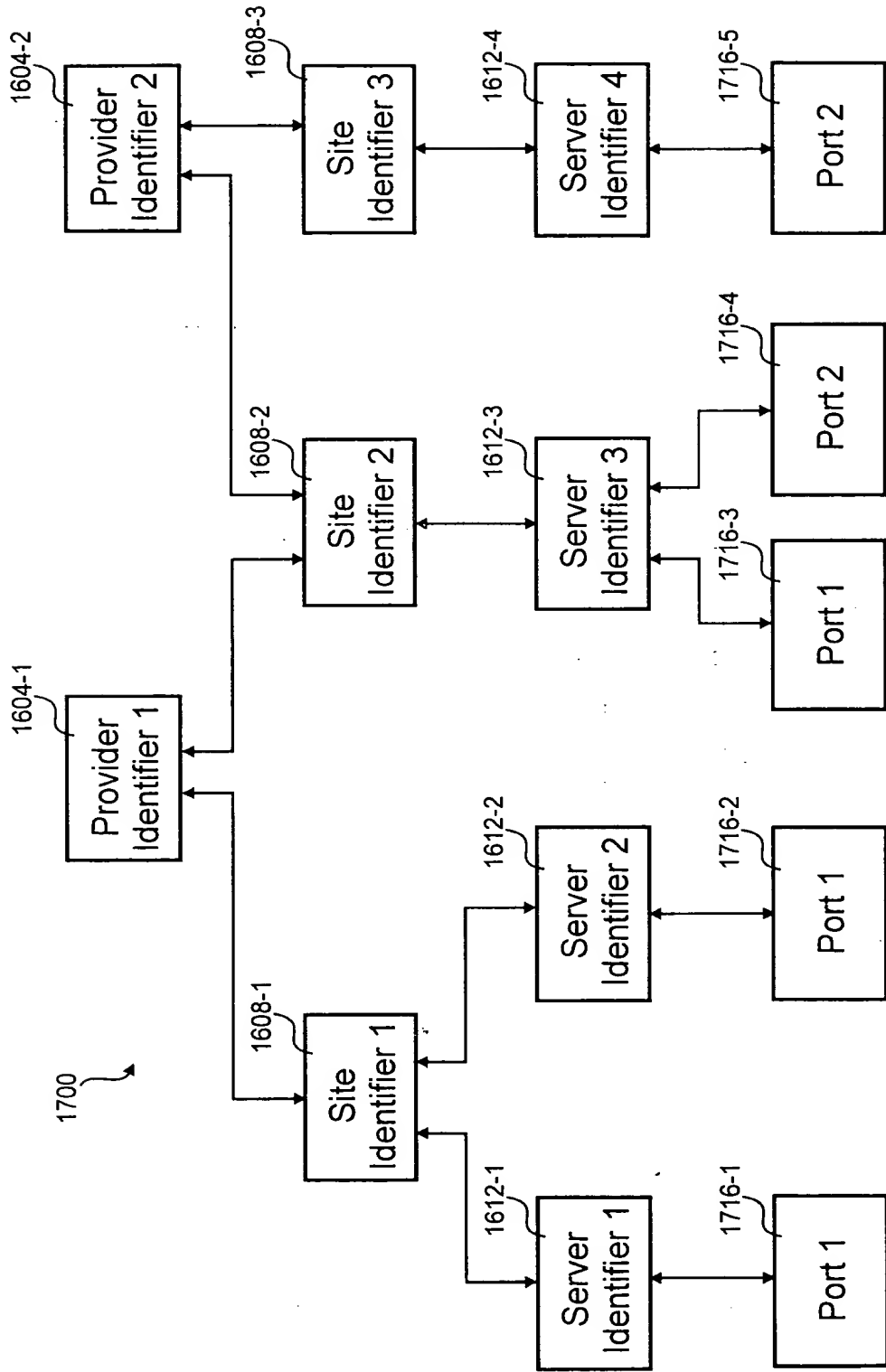


Fig. 17